

FIG. 1

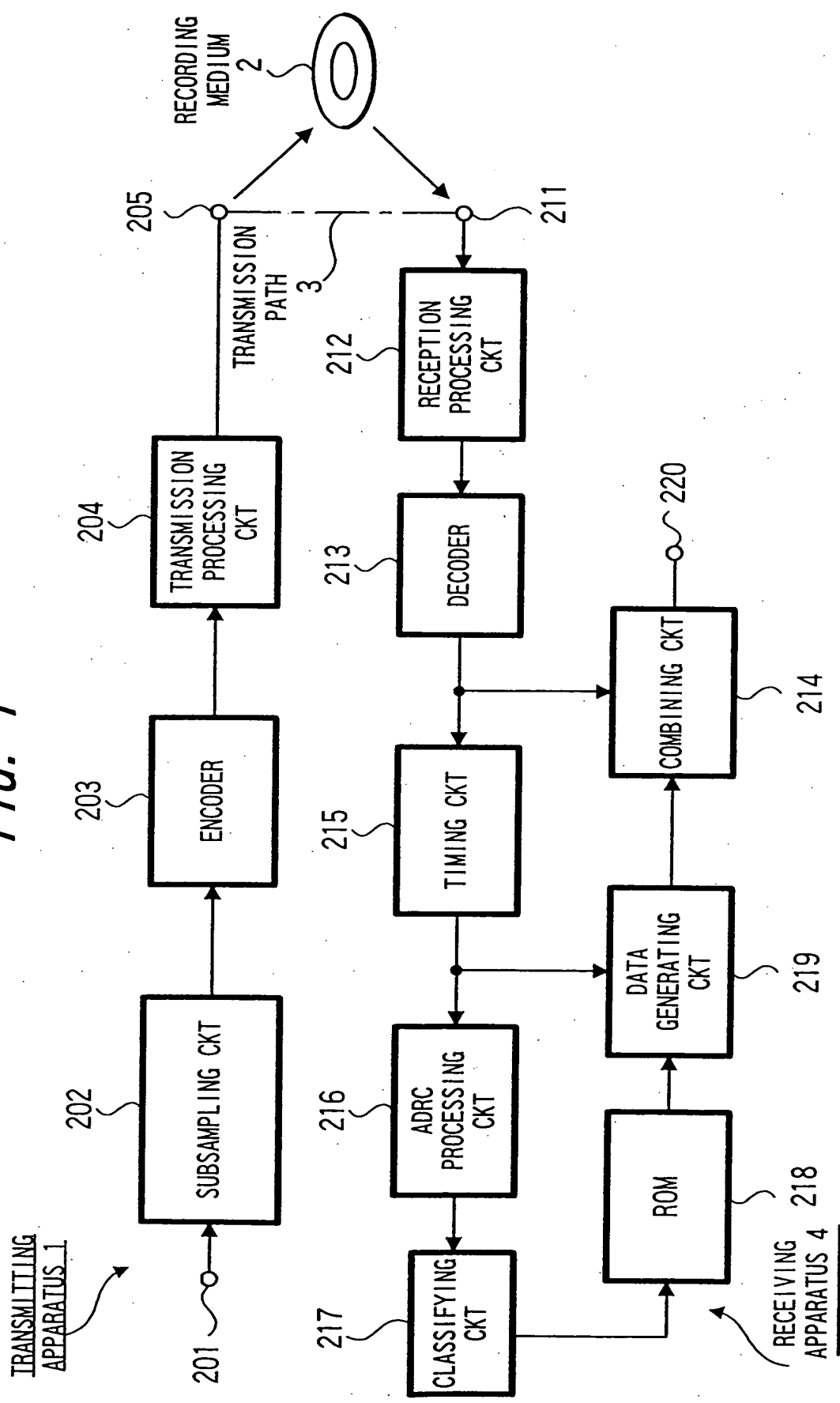


FIG. 2

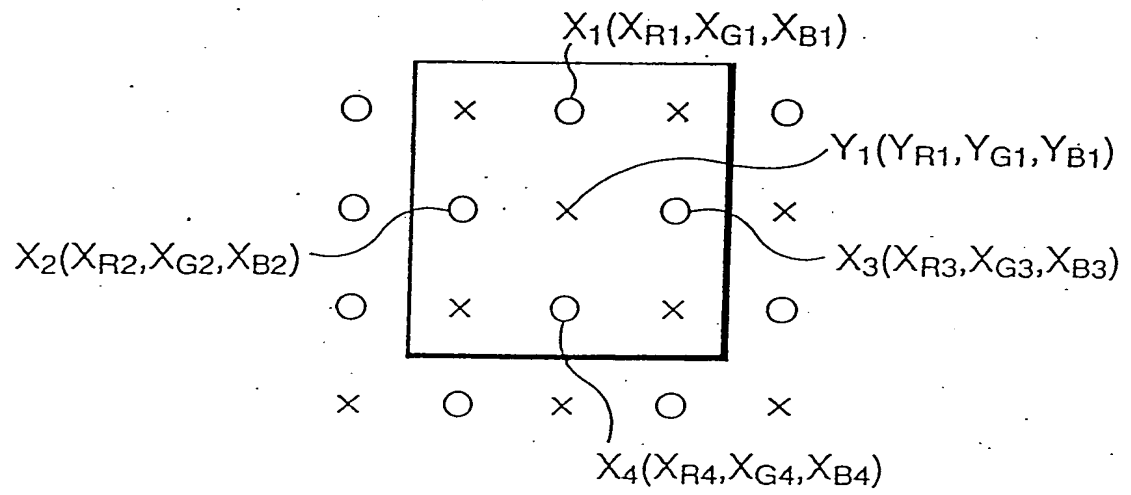
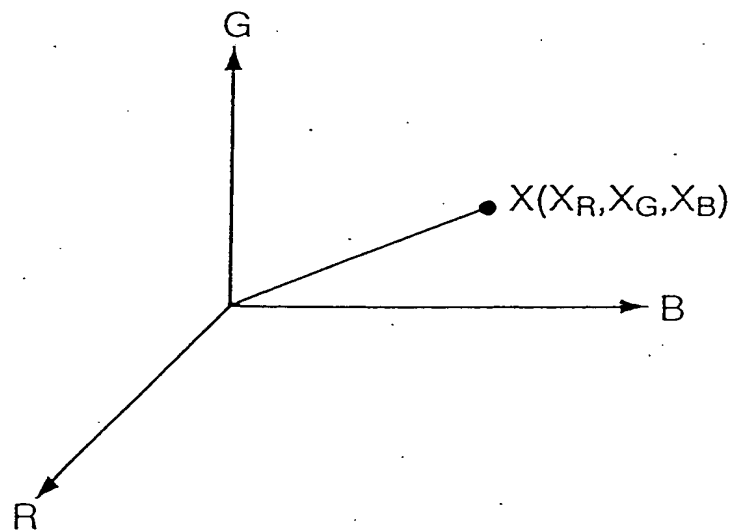


FIG. 3



COLOR SPACE

FIG. 4

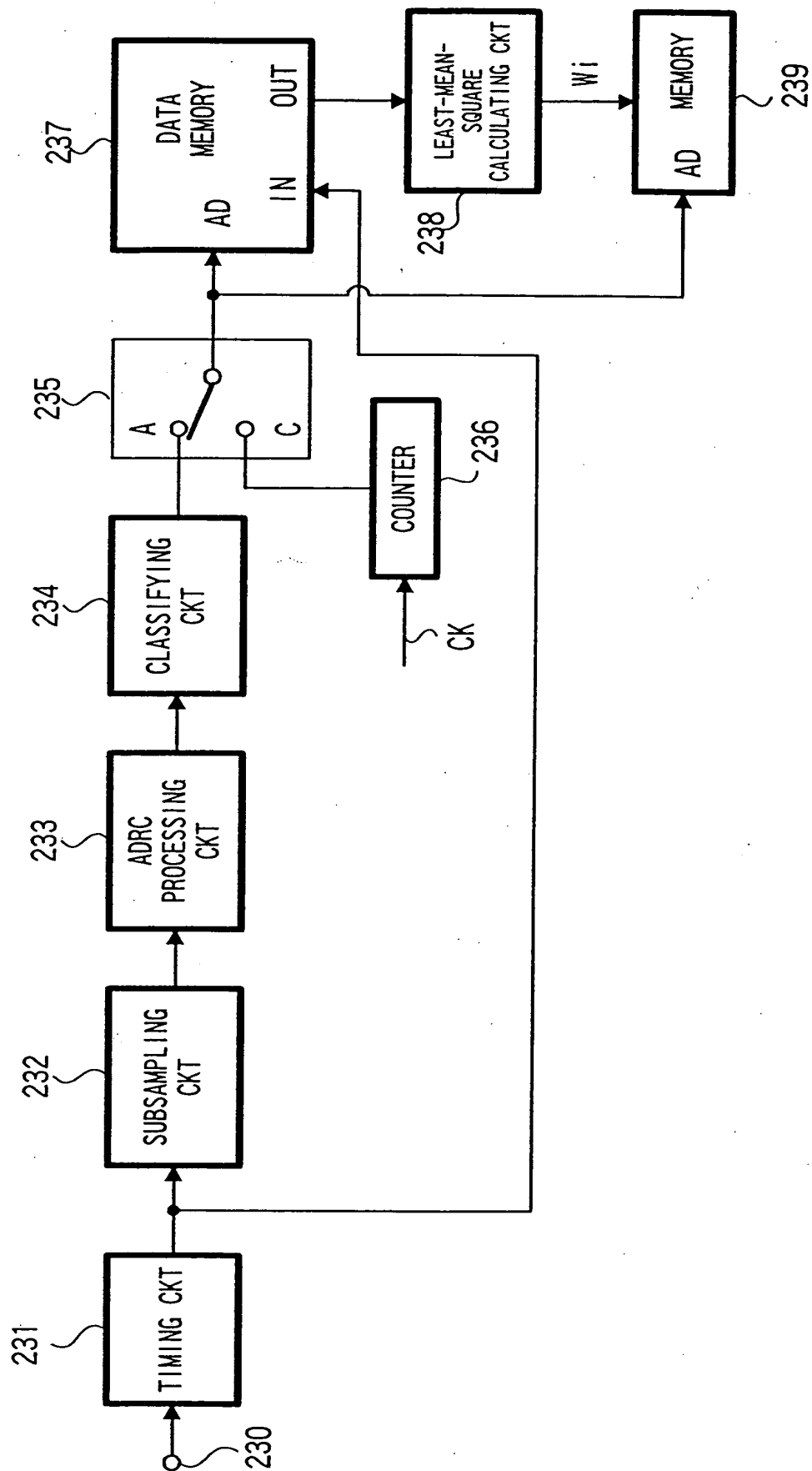


FIG. 5

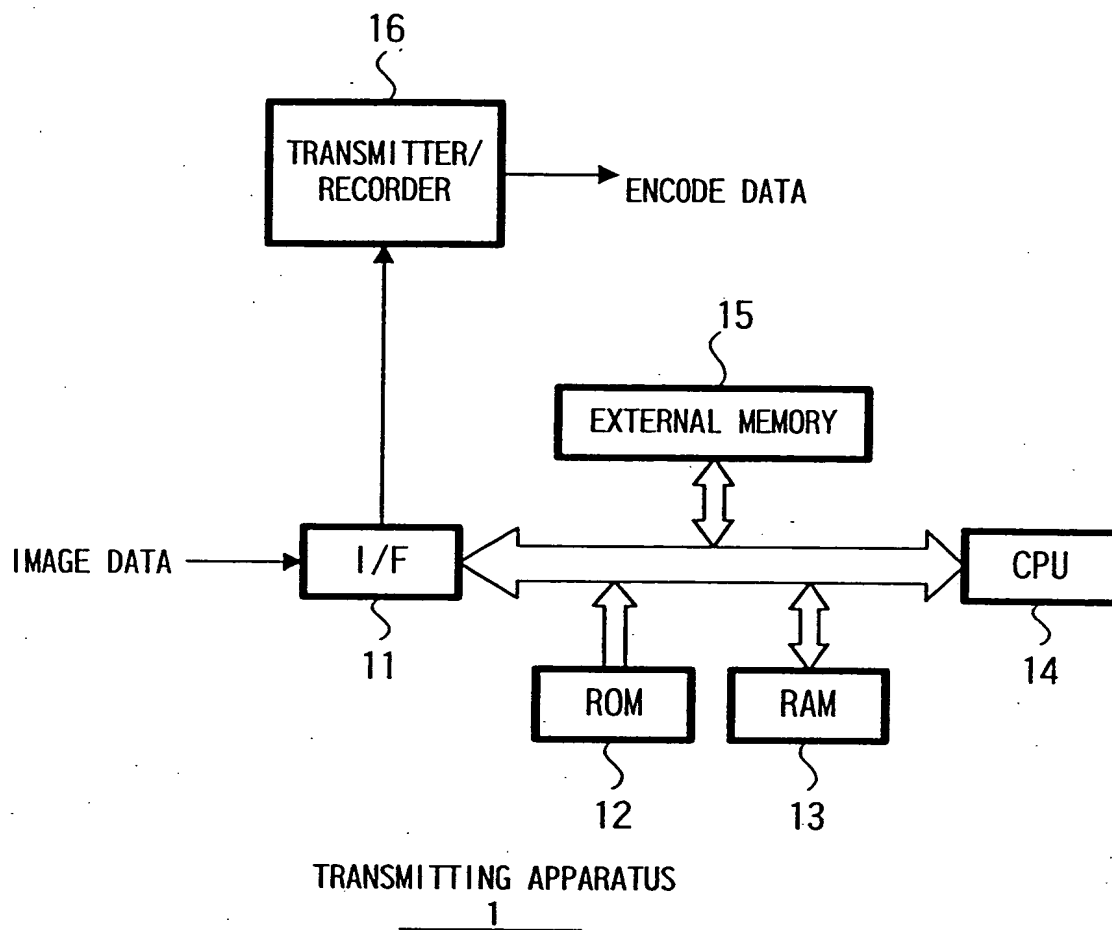


FIG. 6

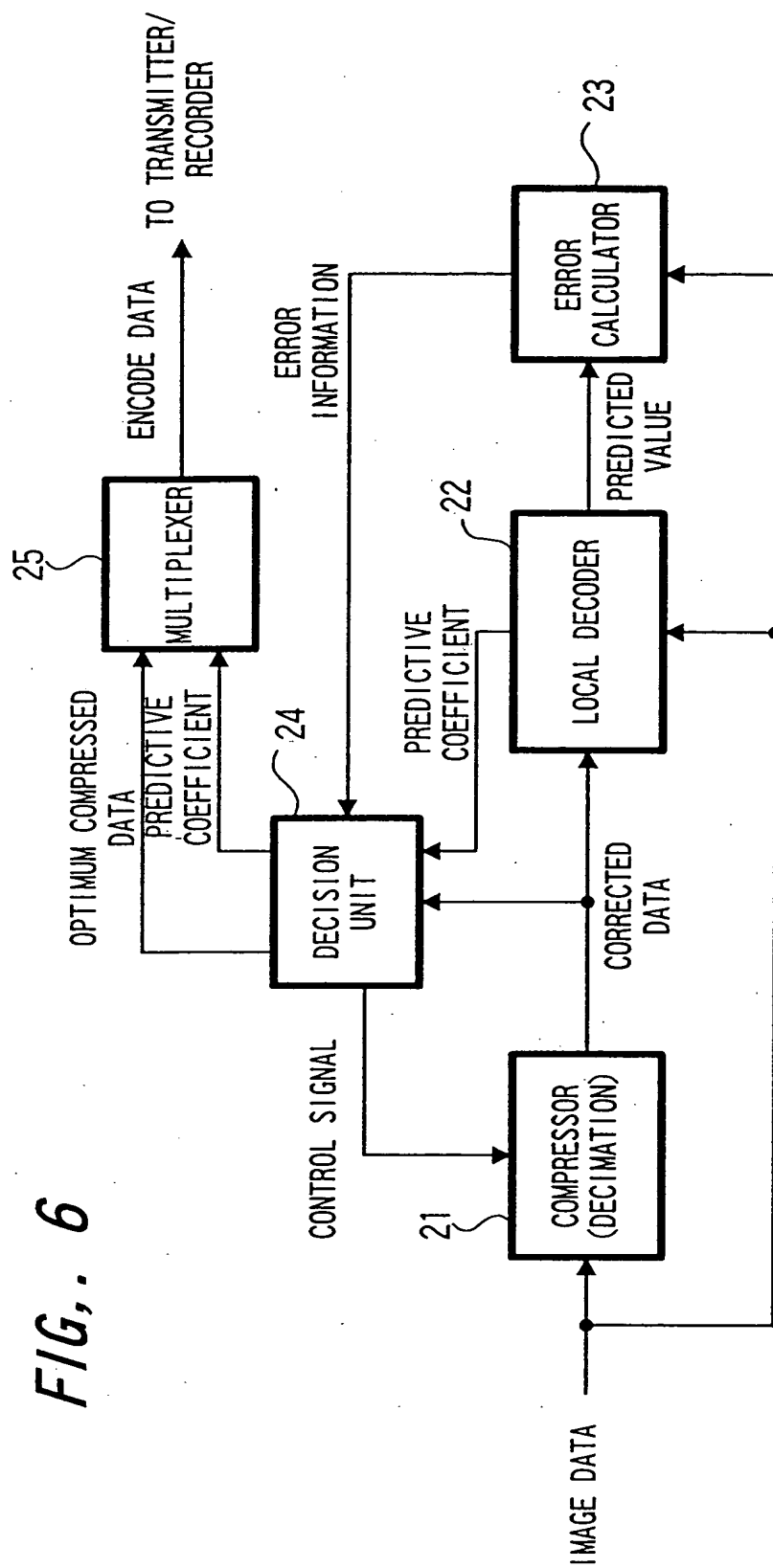


FIG. 7

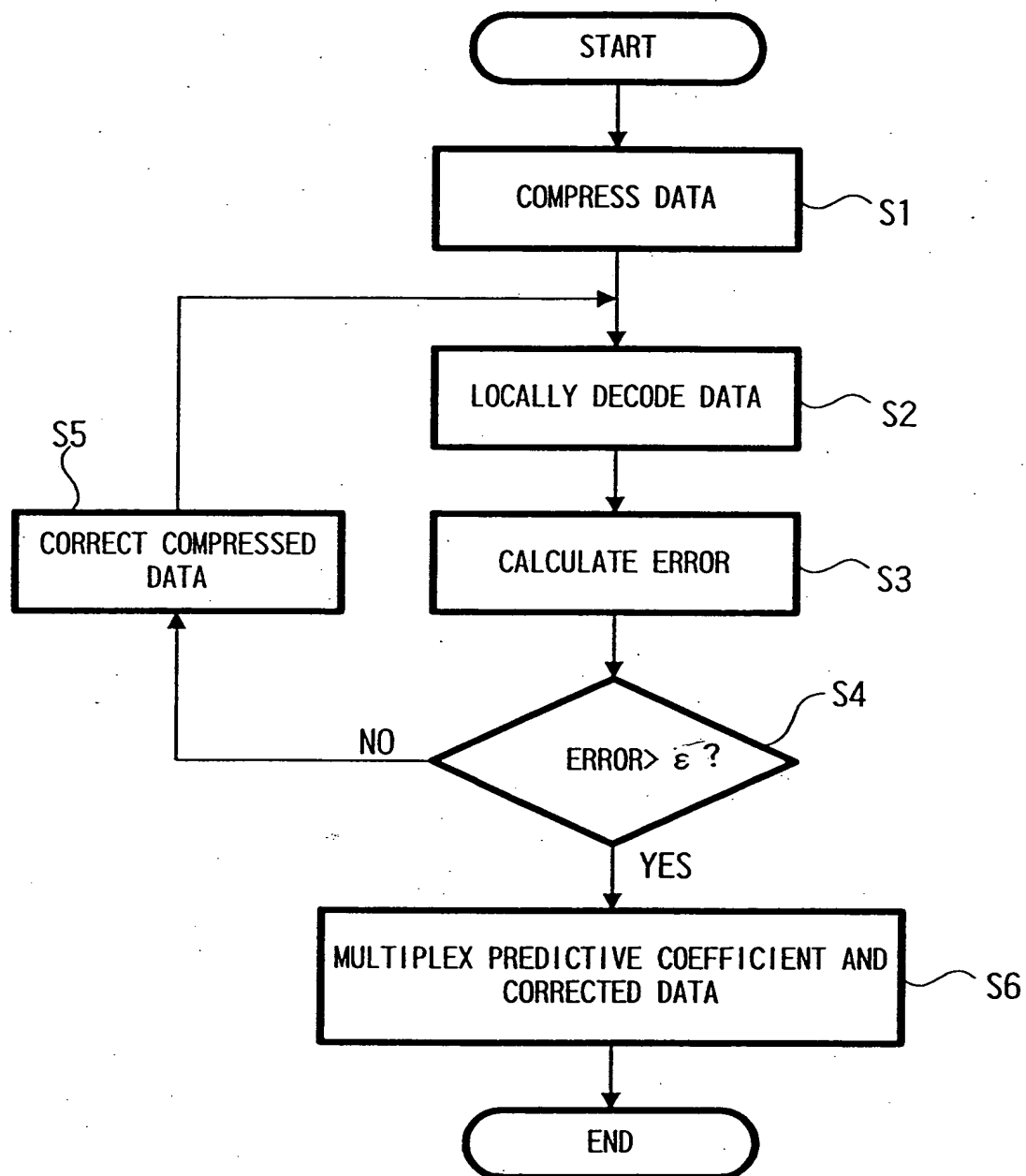
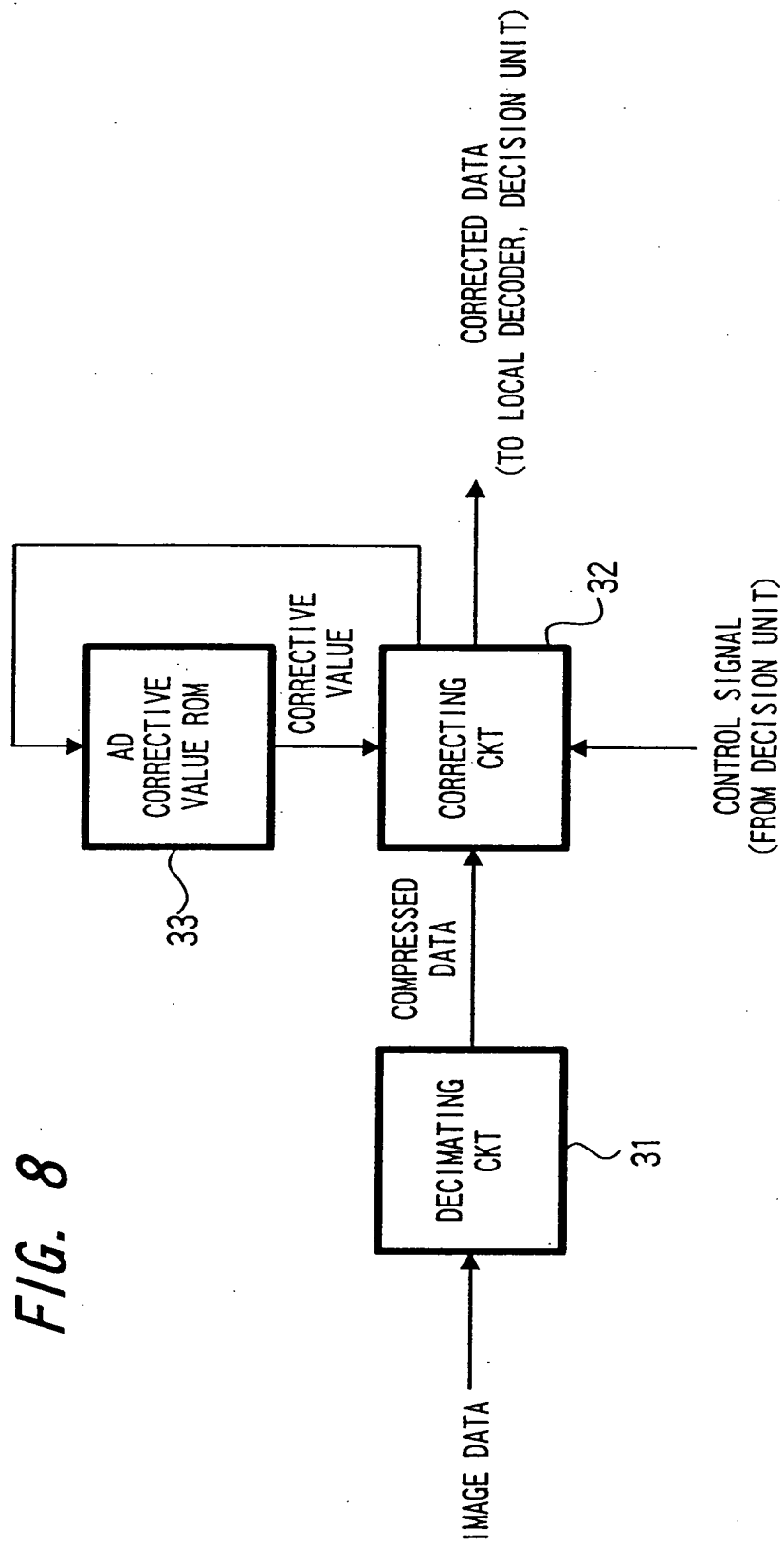


FIG. 8



COMPRESSOR

21

FIG. 9

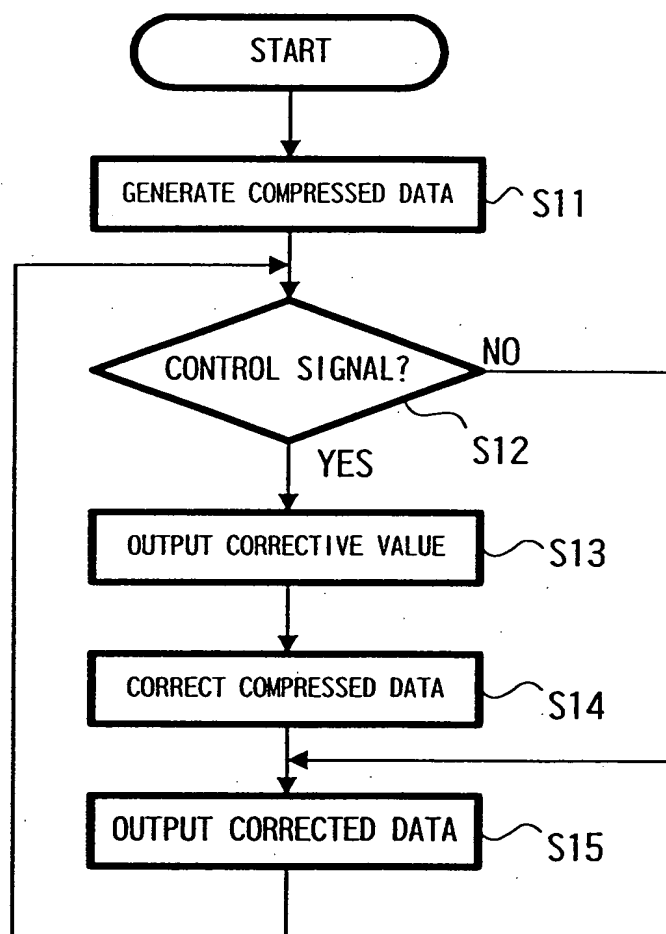


FIG. 10

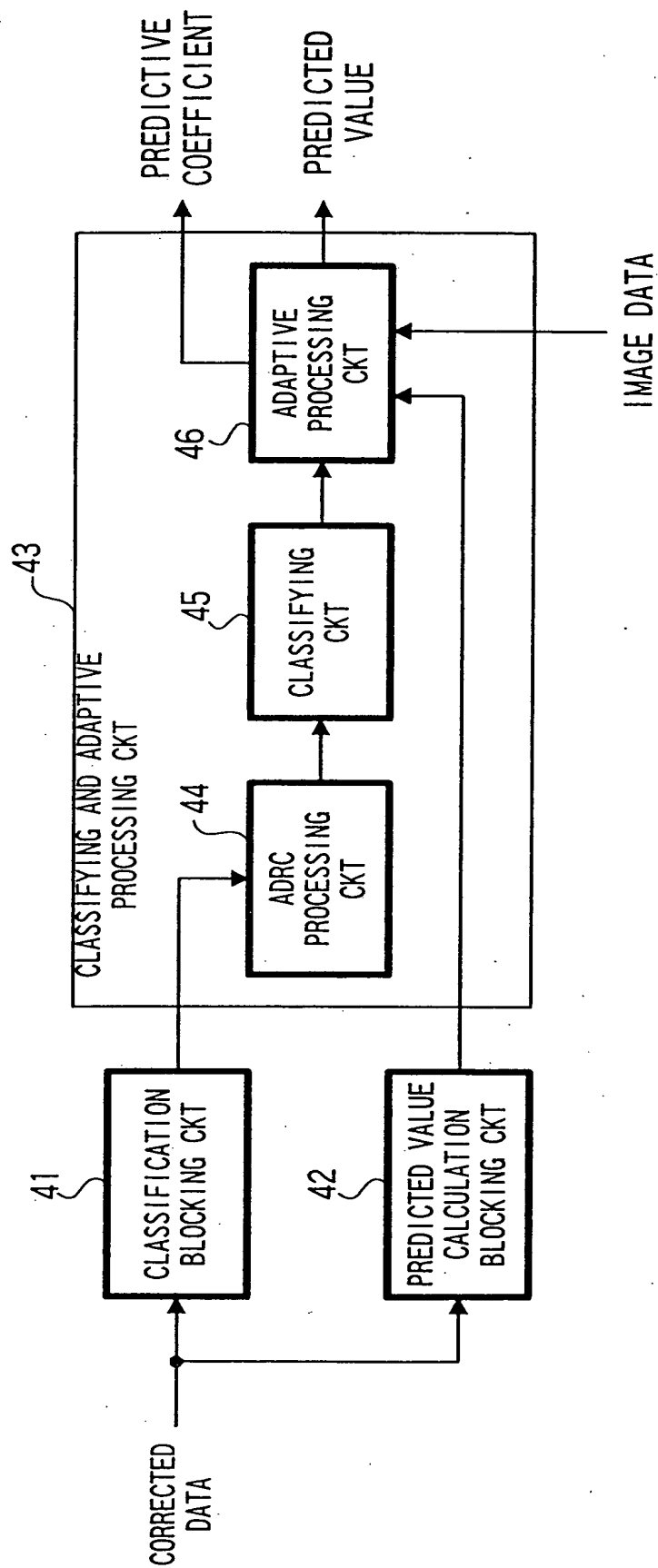


FIG. 11A

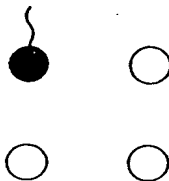
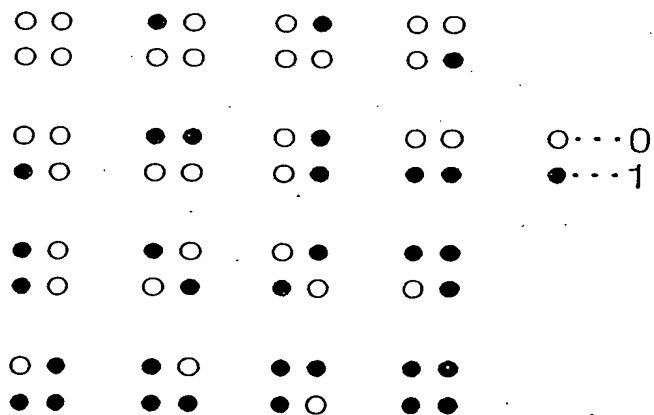


FIG. 11B



CLASSIFYING PROCESS

FIG. 12A

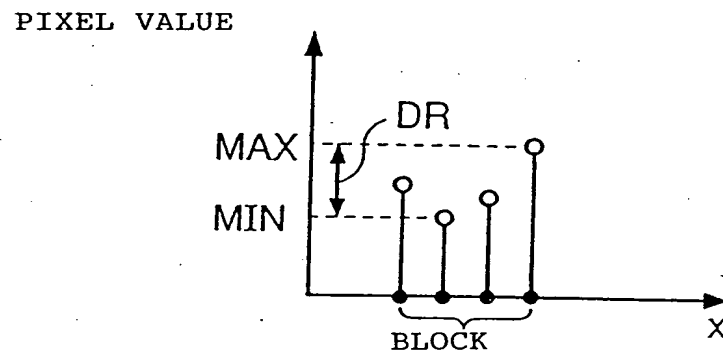
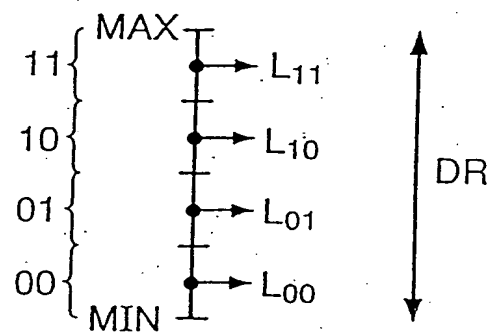
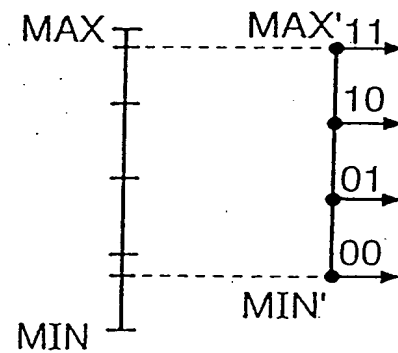


FIG. 12B



NON-EDGE MATCHING

FIG. 12C



EDGE MATCHING

ADRC PROCESS

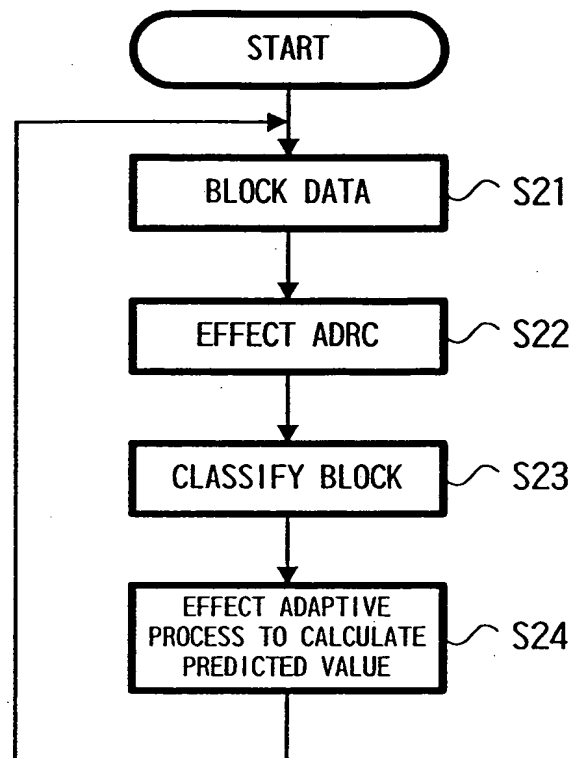
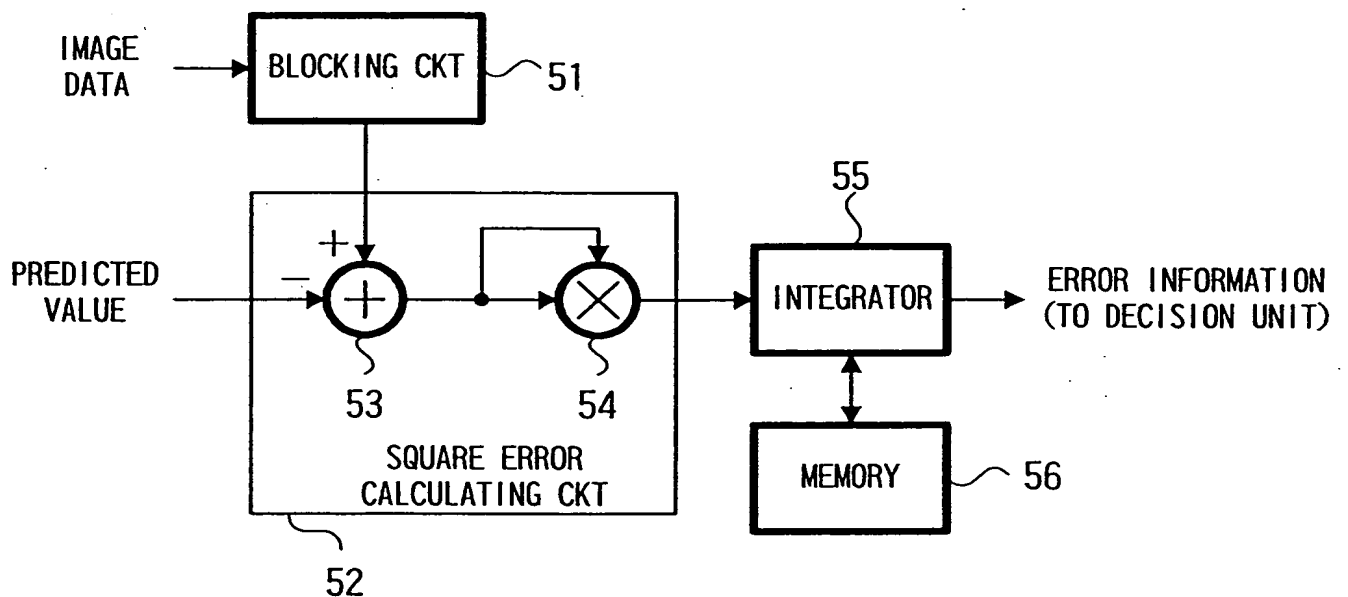


FIG. 14



ERROR CALCULATOR 23

FIG. 15

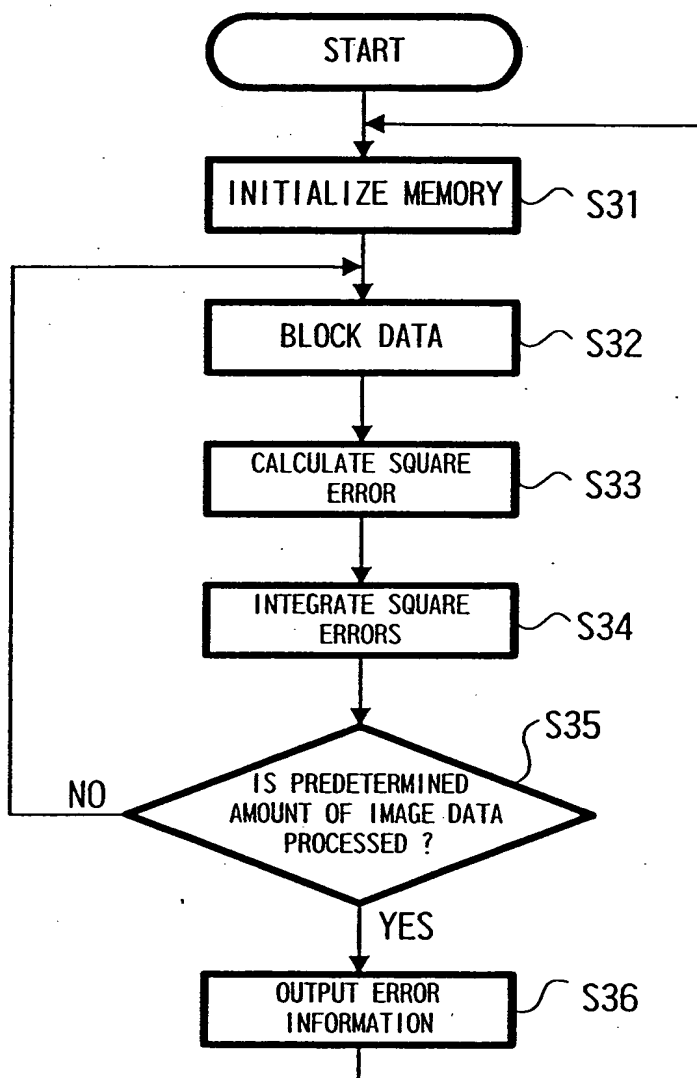


FIG. 16

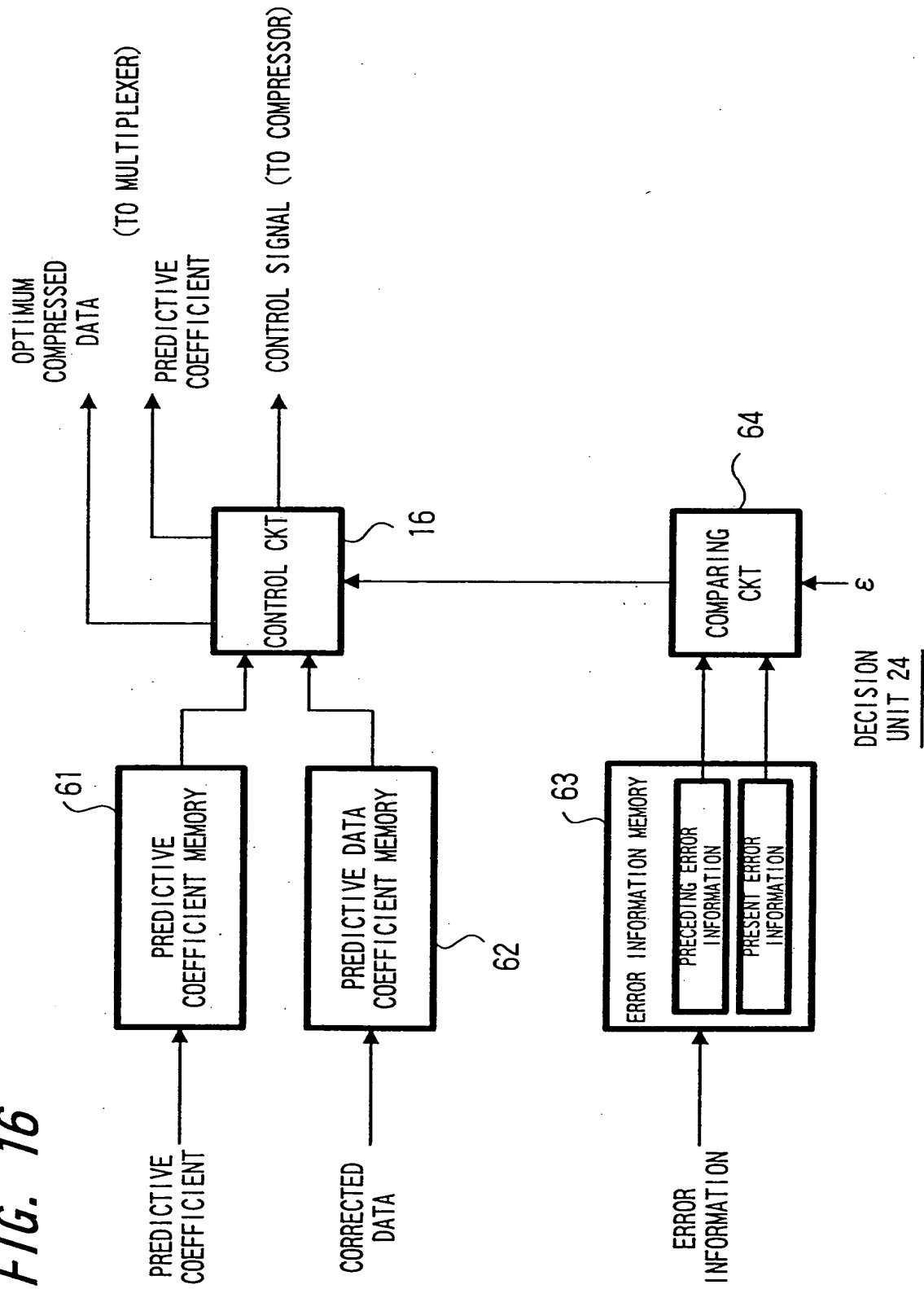


FIG. 17

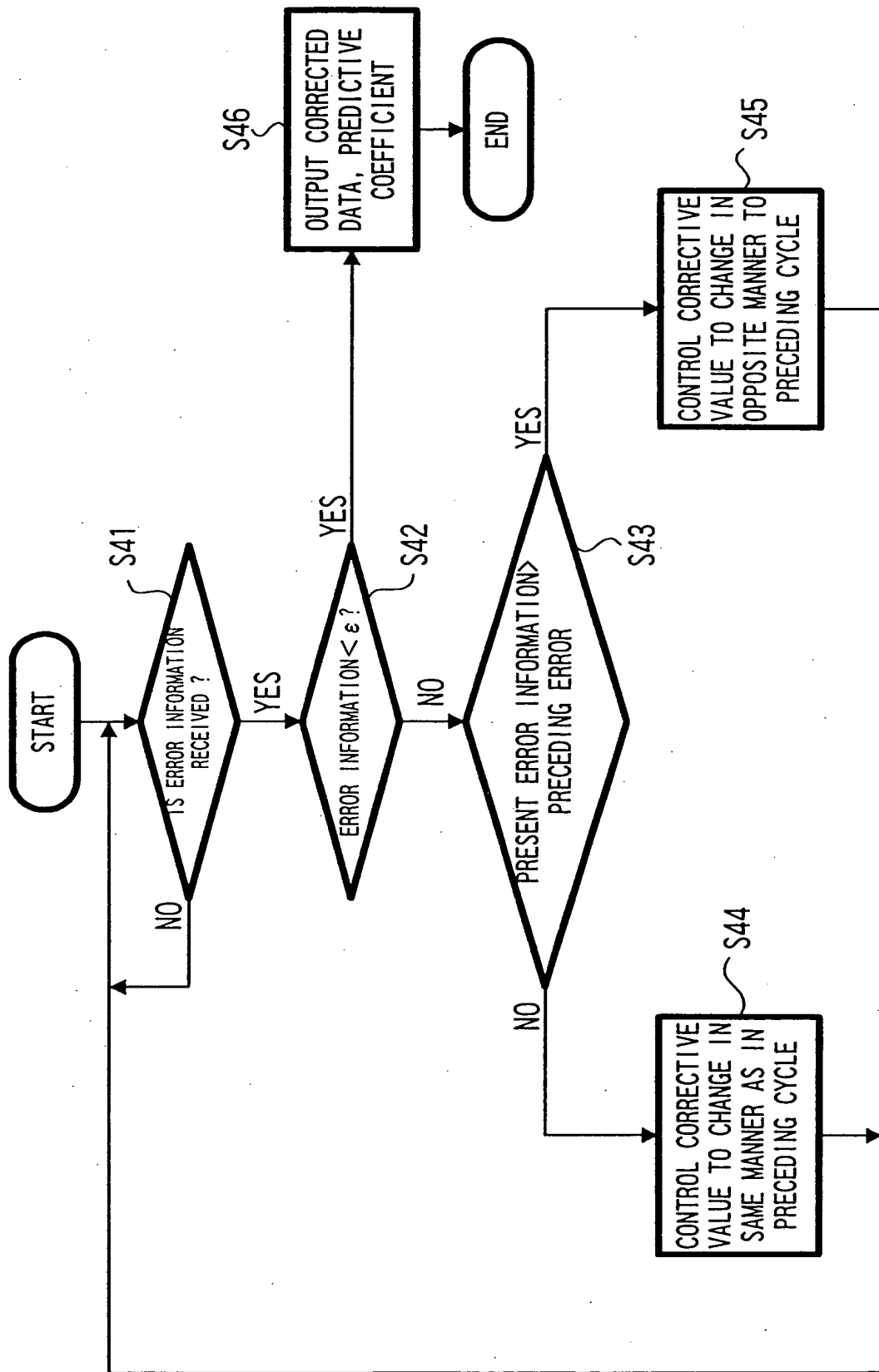


FIG. 18

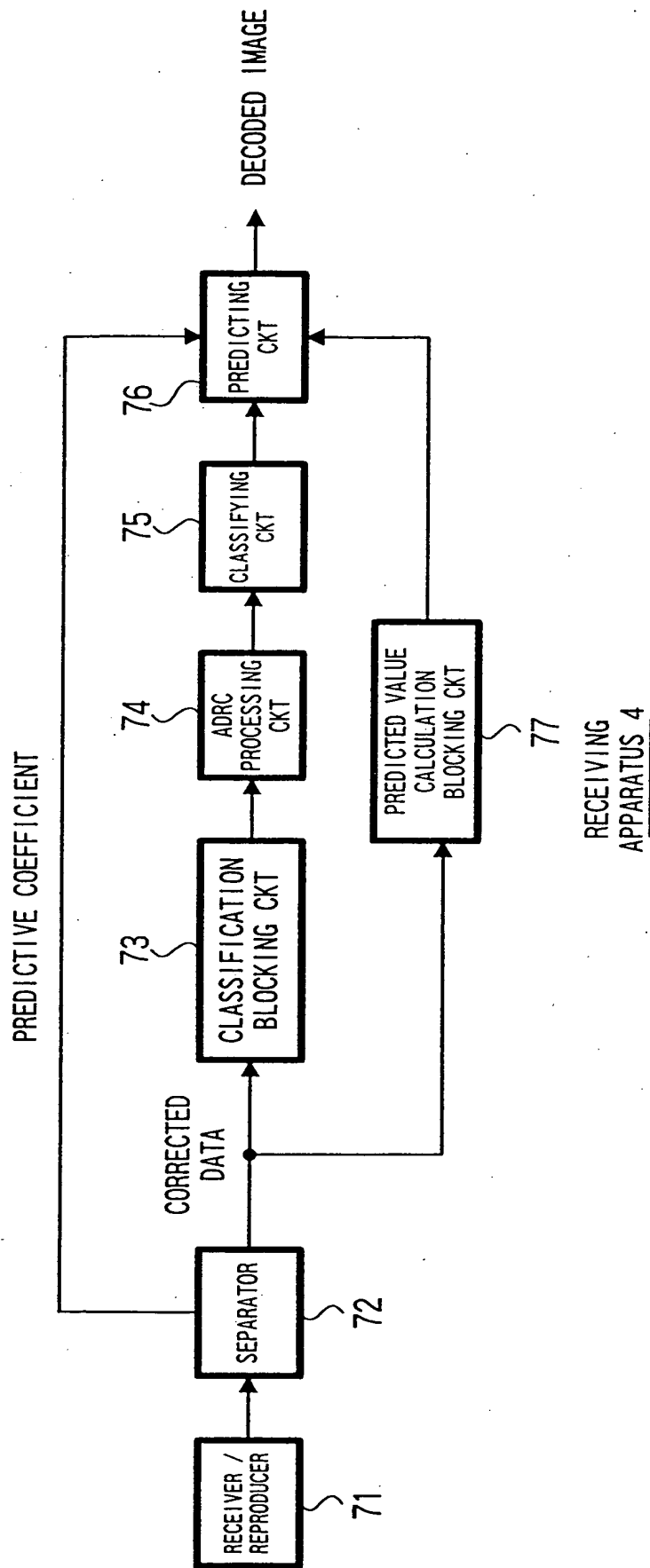


FIG. 19

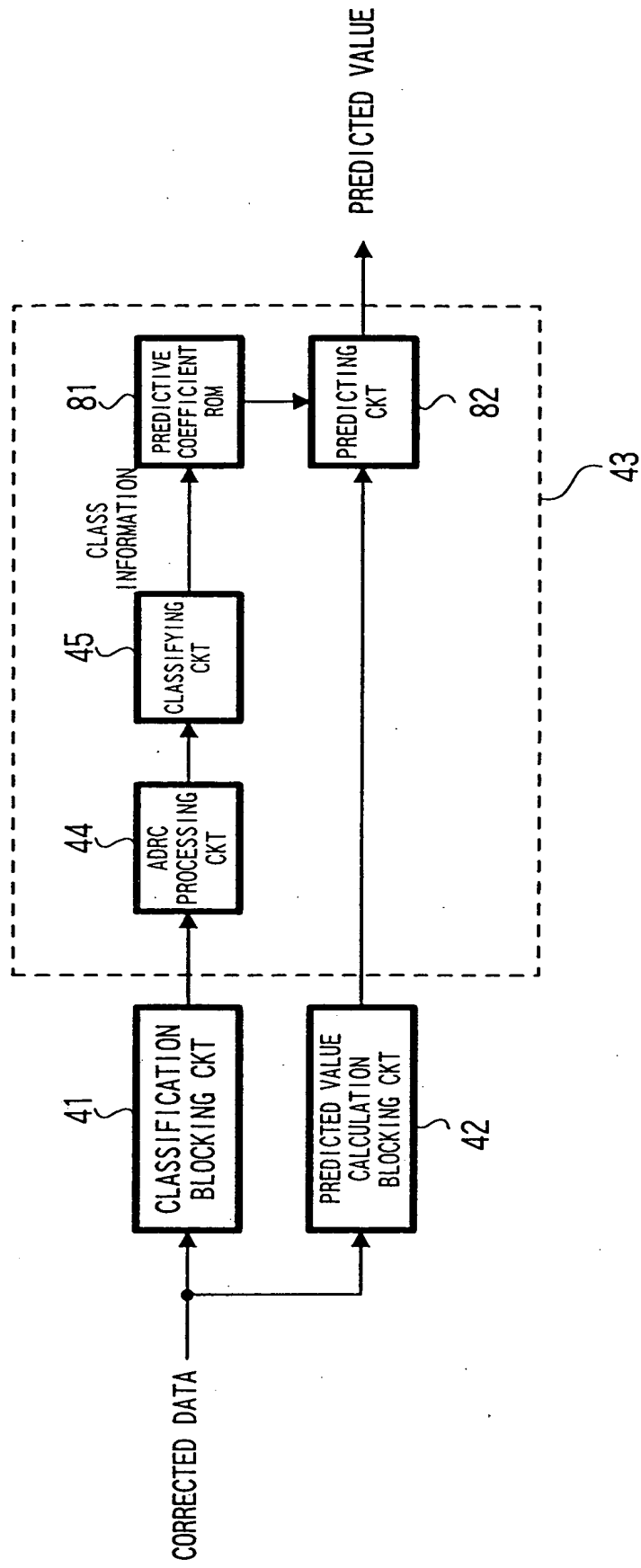


FIG. 20

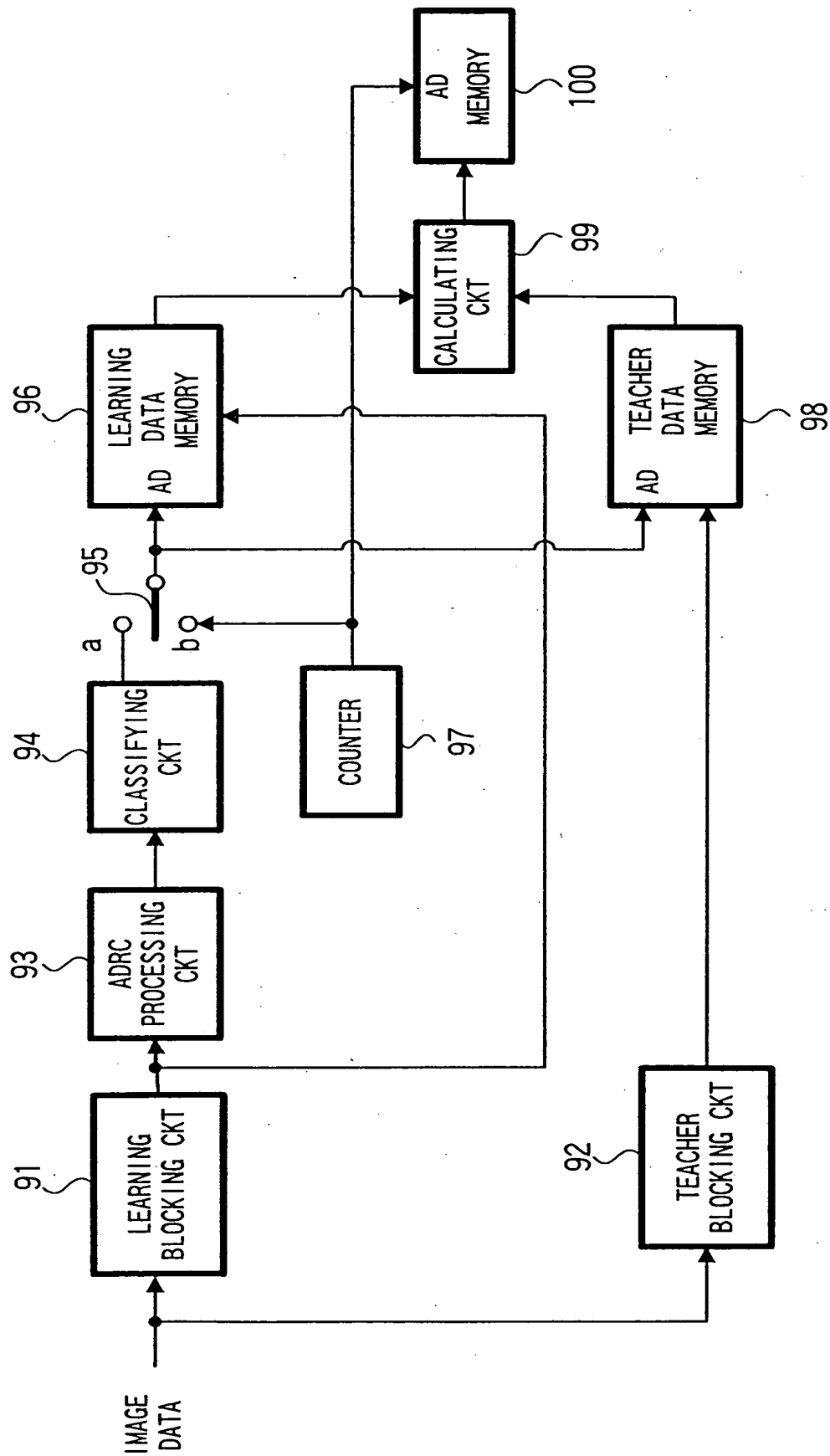


FIG. 21

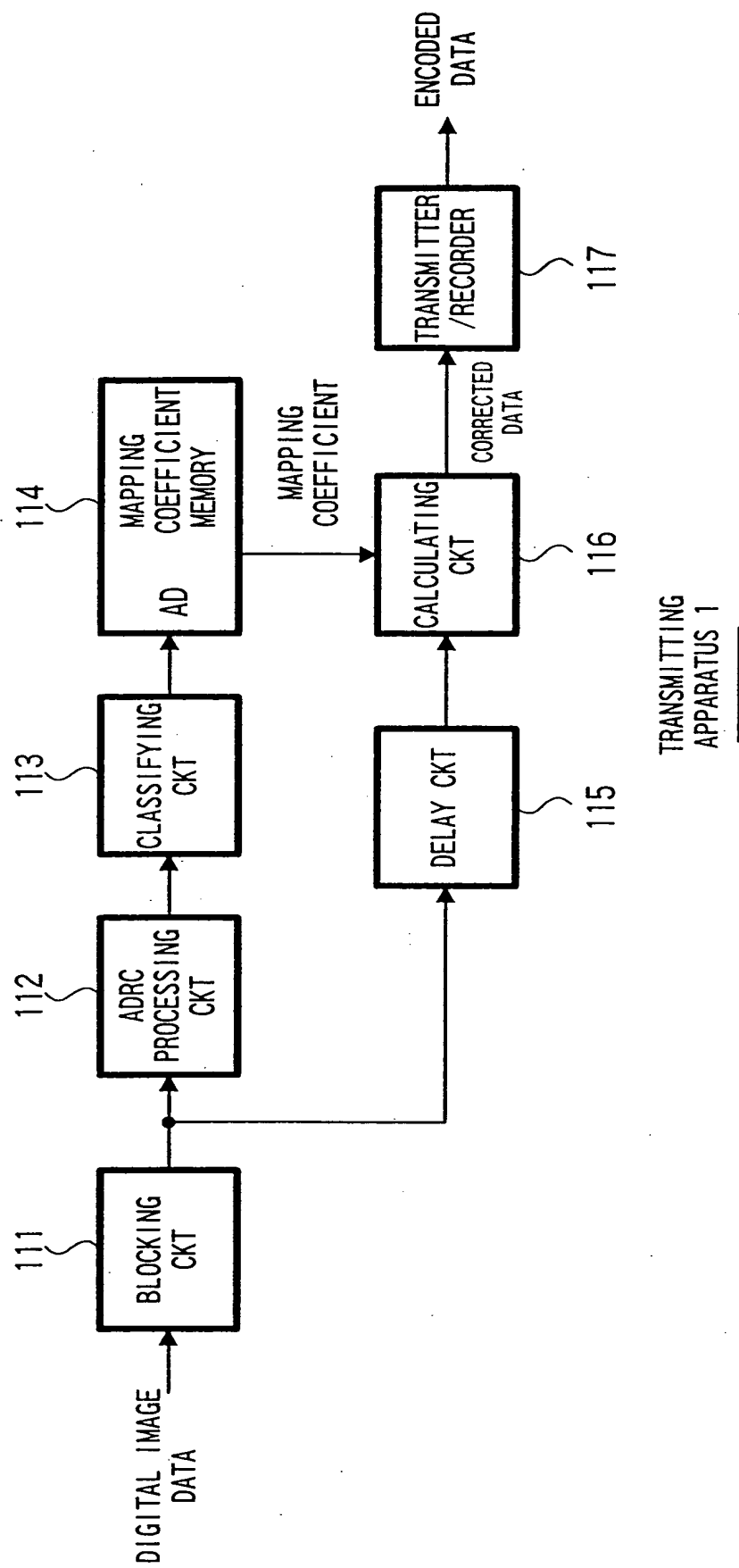


FIG. 22

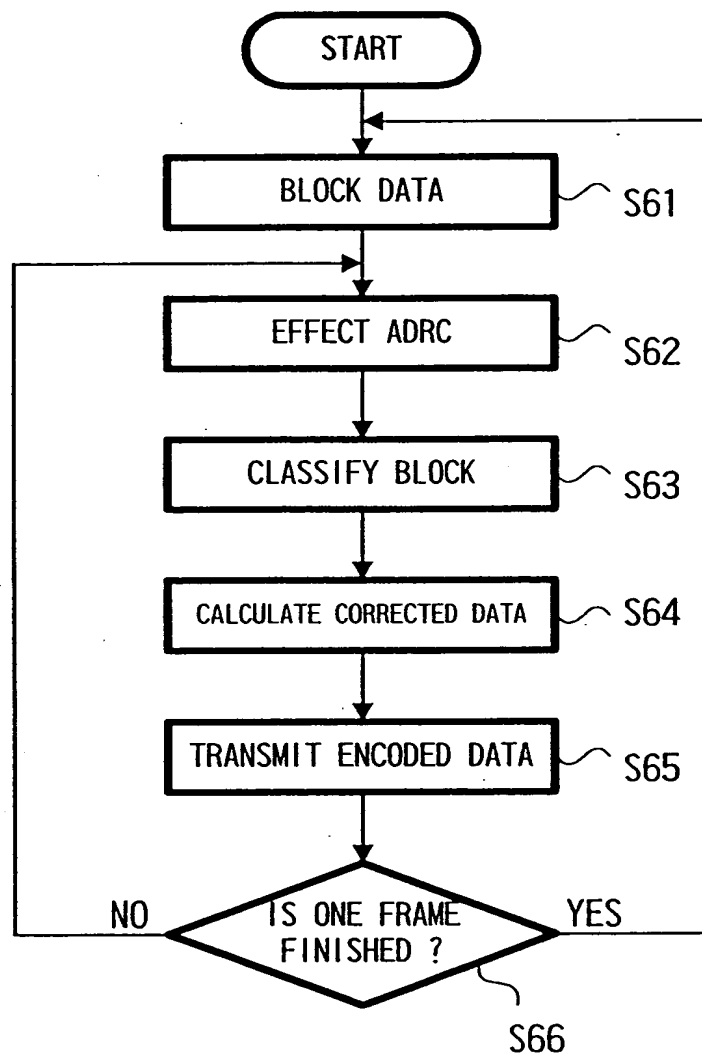


FIG. 23

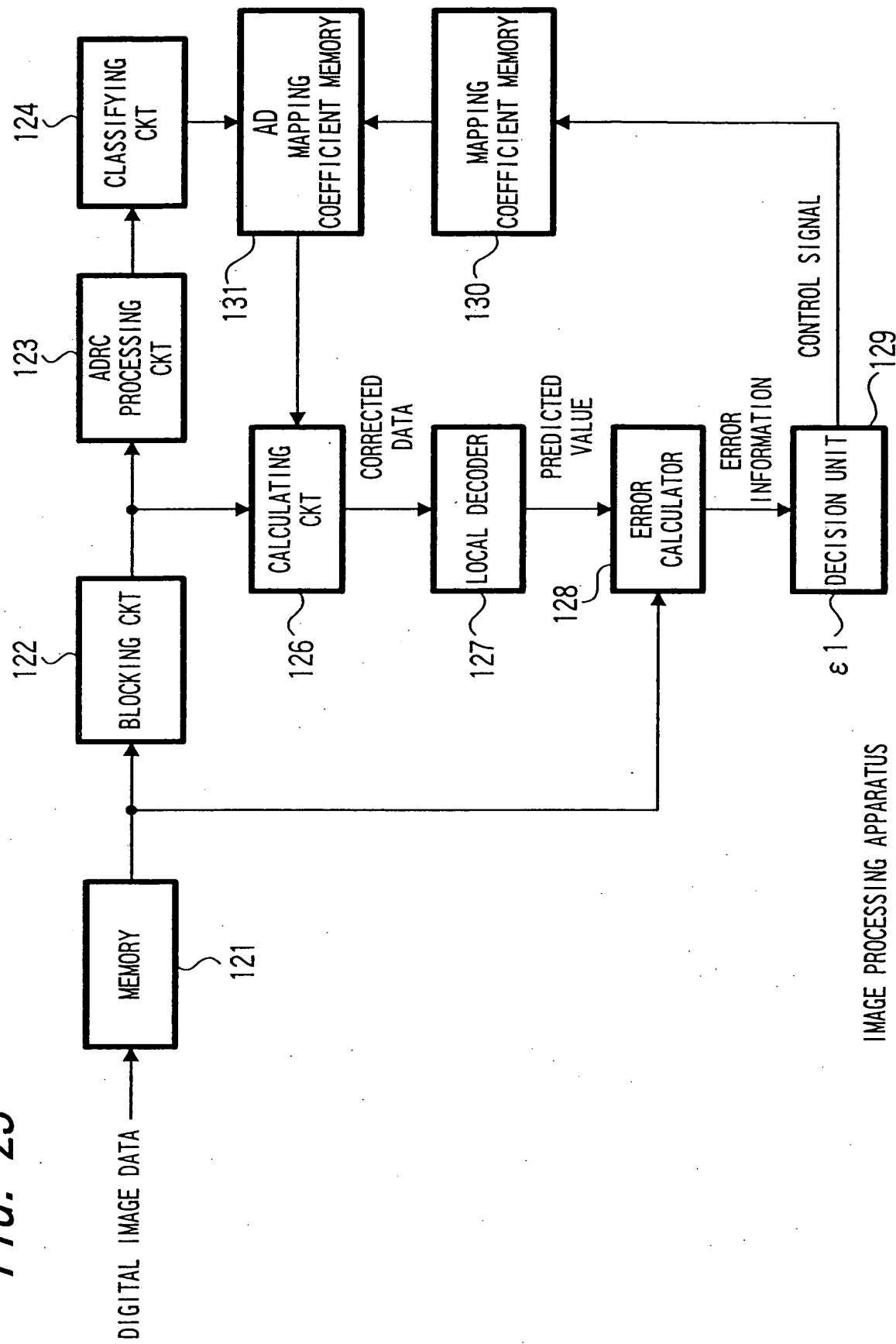


IMAGE PROCESSING APPARATUS

FIG. 24

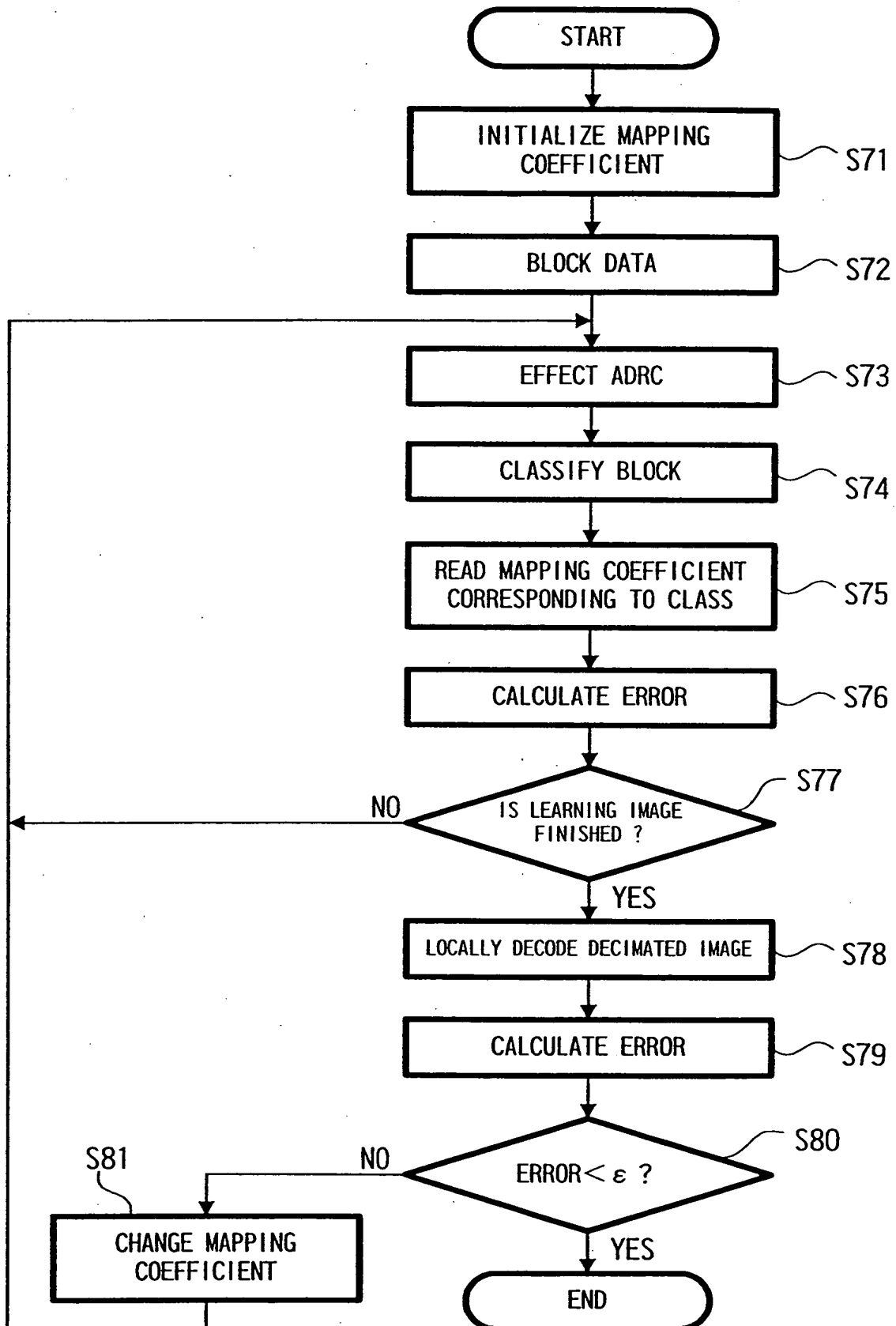


FIG. 25

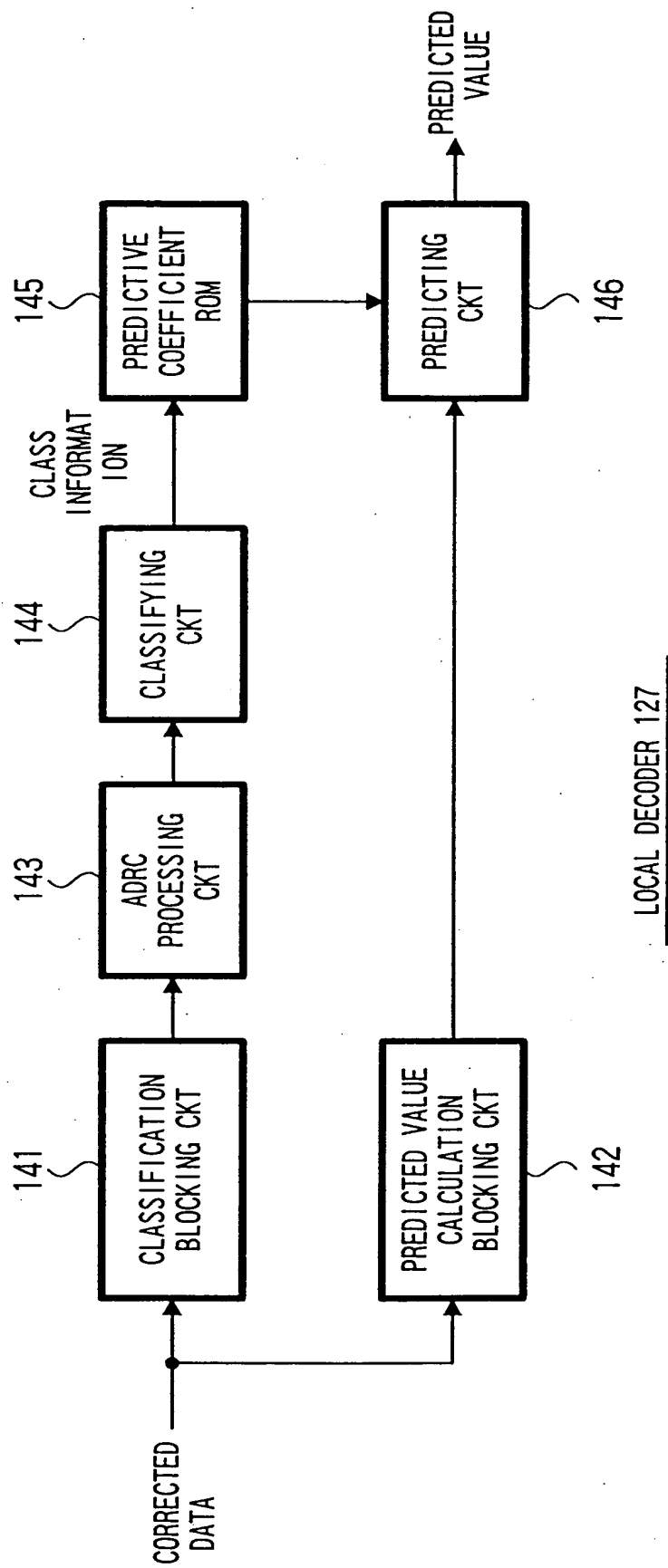


FIG. 26

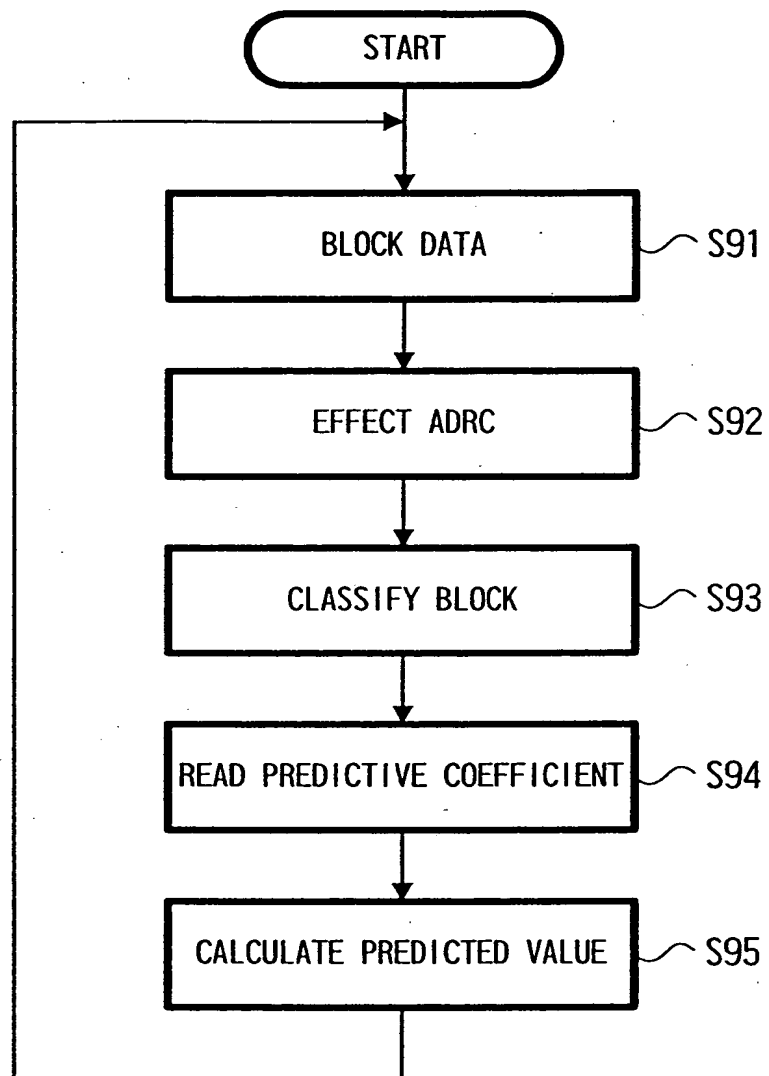


FIG. 27

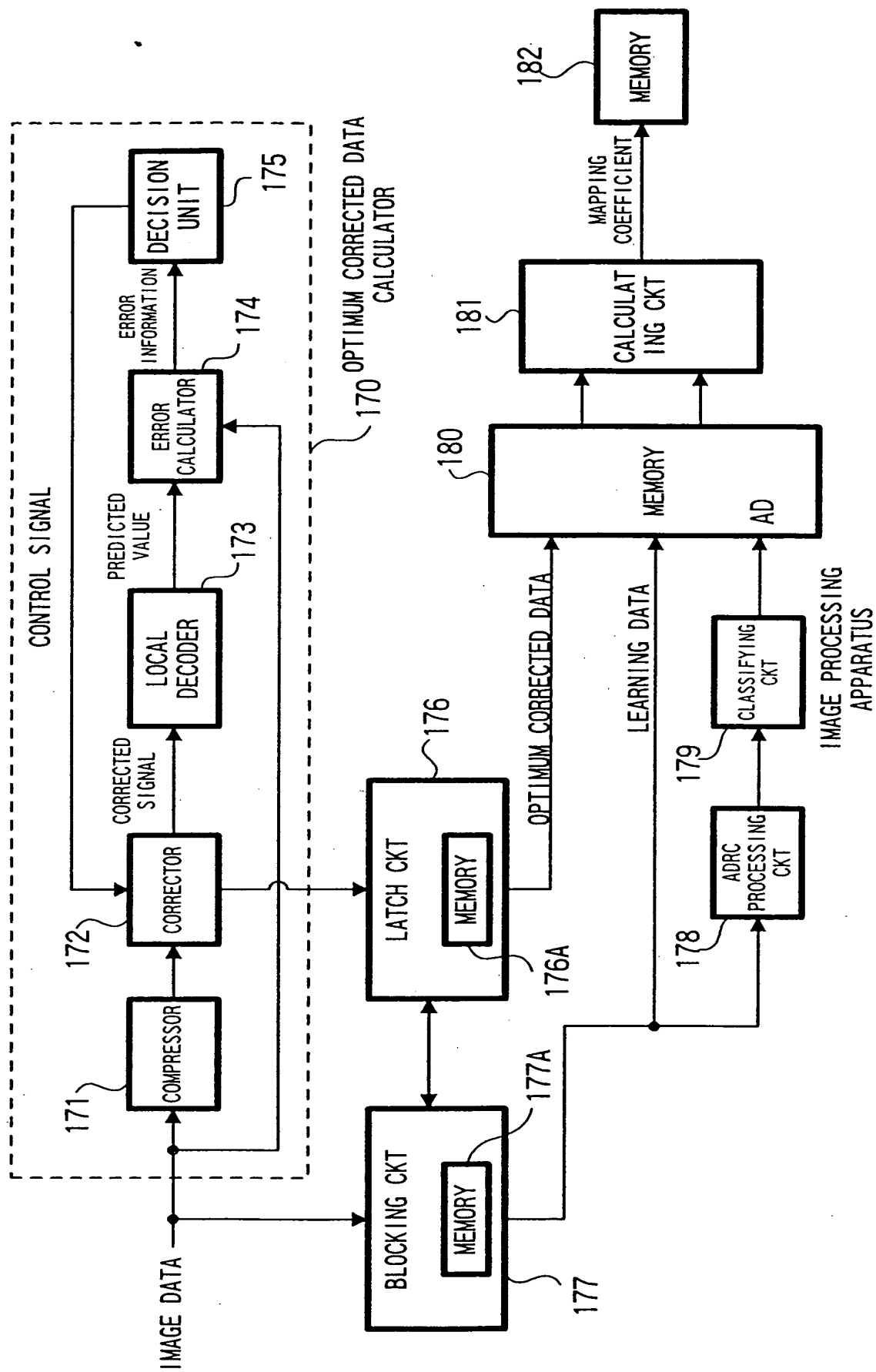


FIG. 28

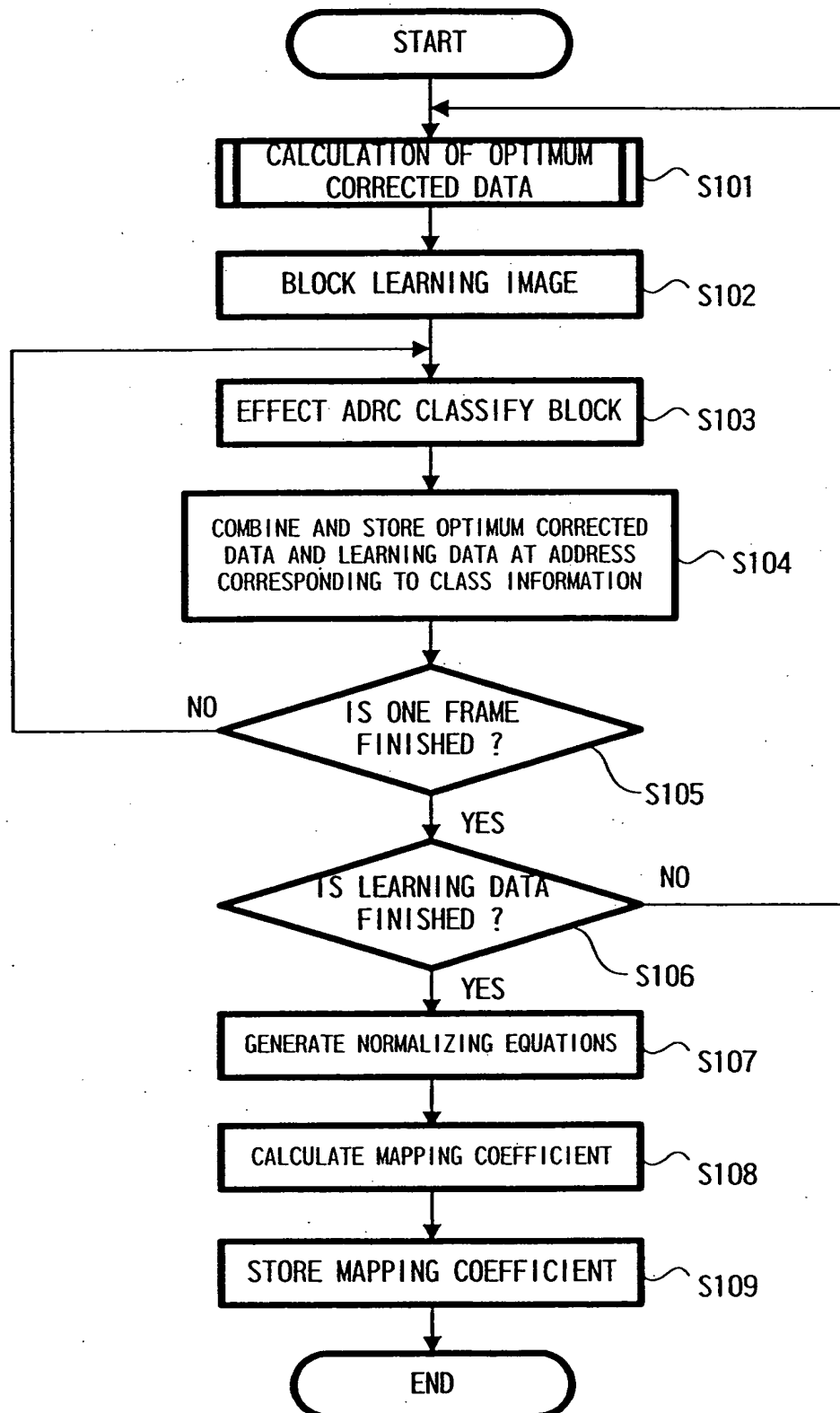


FIG. 29

